

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Ifu

In re Patent Application of:
MAGRI' ET AL.



Serial No. 10/749,134

Confirmation No. 3843

Filing Date: DECEMBER 30, 2003

For: VERTICAL-CONDUCTION AND
PLANAR-STRUCTURE MOS DEVICE WITH
A DOUBLE THICKNESS OF GATE OXIDE)
AND METHOD REALIZING POWER VERTICAL)
AND TRANSISTORS WITH IMPROVED
STATIC AND DYNAMIC PERFORMANCES)
AND HIGH SCALING DOWN DENSITY)

REQUEST FOR CORRECTION TO THE FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The filing receipt for the subject patent application has been received and reviewed for accuracy. A discrepancy has been discovered: The filing receipt in error lists the foreign priority data filing date as "12/30/2003." The correct filing date for the foreign priority data is 12/30/2002.

In view of the error noted, it is respectfully requested that a corrected filing receipt be issued in this application. If any fee is believed to be necessary, authorization is given to charge Deposit Account No. 01-0484.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Christopher F. Regan".

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: **COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450**, on this 4th day of October, 2005.

Justin Dan



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/749,134	12/30/2003	2811	1274	02CT20753422	8	36	4

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1401 CITRUS CENTER 255 SOUTH ORANGE AVENUE
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CONFIRMATION NO. 3843

UPDATED FILING RECEIPT



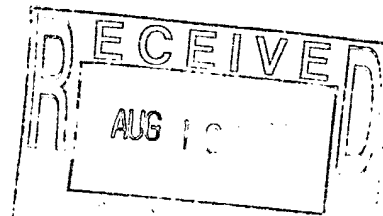
OC000000013452828

Date Mailed: 08/05/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Assignment For Published Patent Application

STMicroelectronics S.r.l., Agrate Brianza (MI), ITALY;

Domestic Priority data as claimed by applicant

Foreign Applications

EUROPEAN PATENT OFFICE (EPO) 02425803.0 12/30/2003

12/30/2002

If Required, Foreign Filing License Granted: 04/29/2004

Projected Publication Date: 06/30/2005

Non-Publication Request: No

Early Publication Request: No

Title

Vertical-conduction and planar-structure MOS device with a double thickness of gate oxide and method for realizing power vertical MOS transistors with improved static and dynamic performances and high scaling down density

Preliminary Class

257

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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